(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(54) Title: A PROCESSOR AND A METHOD IN THE PROCESSOR, THE PROCESSOR COMPRISING A PROGRAMMABLE PIPELINE AND AT LEAST ONE INTERFACE ENGINE
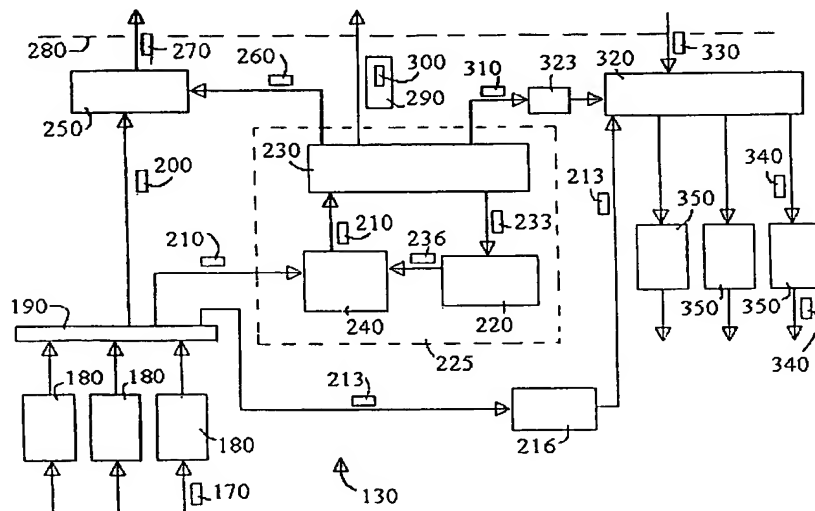
(57) Abstract: A processor is presented, comprising a programmable pipeline and at least one interface engine (130), adapted to be connected to at least one external device (140) located externally of the processor. The processor is characterized in that the interface engine (130) is adapted to receive a request (170) from the programmable pipeline, to send to the external device (140) a request output (270), based on the request (170), and to send to the pipeline a response (340) to the request (170). Preferably, the request (170) comprises a first request code (210), according to a first coding scheme, and the interface engine (130) is adapted to execute a program, the execution being dependent upon the first request code, to obtain a device control code (300) for the external device (140), according to a second coding scheme.

WO 2004/010287 A1